



PATENT APPLICATION
Q62963

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q62963

Hidetaka NAMBU

Appln. No. 09/836,286

Group Art Unit: 1765

Confirmation No.: 5457

Examiner: Duy Vu DEO

Filed: April 18, 2001

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF THE DEVICE

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated May 30, 2002 in which the Examiner has required a restriction, Applicant responds by electing Group I, *i.e.*, claims 1-19, drawn to a method, without traverse. This election is made without prejudice to the filing of a divisional application directed to claim 20 .

Prompt and favorable action on the elected claims is now respectfully requested.

Respectfully submitted,

Howard L. Bernstein
Registration No. 25,665

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JUL 5 - 2002

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Date: July 1, 2002